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PATENT NUMBER and  
ISSUE DATE

## U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10065694	11/11/2002	714	718	2133	<i>Decker</i>

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**\*\*CONTINUING DATA VERIFIED:**

**\*\* FOREIGN APPLICATIONS VERIFIED:**

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		BUR920010195
Verified and Acknowledged Examiners's initials		
TITLE : Method for testing embedded DRAM arrays		

U S DEPT OF COMM /PAT. & TM-PTO-436L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G
Assistant Examiner		<b>DRAWING</b>	
		Sheets Drwg.	Figs.Drwg.
Amount Due		Print Fig.	
Date Paid			
Primary Examiner		Application Examiner	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	
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